



# The International Conference on Dependable Systems and Networks (DSN 2006)

June 25-28, 2006 Sheraton Society Hill, Philadelphia, Pennsylvania, USA

Sponsored by:

IEEE Computer Society Technical Committee on Fault-Tolerant Computing  
IFIP WG 10.4 on Dependable Computing and Fault Tolerance



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The 2006 International Conference on Dependable Systems and Networks (DSN 2006), the leading conference on dependable computing since 1971, will be held in Philadelphia, Pennsylvania, USA, from June 25 to 28, 2006. Dependability is a vital attribute of computing and communication systems that pervade every aspect of our daily lives. As we are increasingly relying on reliable and secure functioning of these systems and networks, there have been increasing concerns about malicious exploitation of imperfect systems and networks as well as the traditional concerns for inadvertent or accidental faults, errors, and failures. The focus of this year's conference is on topics related to the question, "How can we make our computer systems and networks more dependable, reliable and trustworthy and thus **make our businesses, homeland and society more safe and secure?**"

The DSN 2006 program offers a keynote talk by a business and technology leader, research papers, industry sessions, panels, tutorials, workshops, fast abstracts, and a student forum. On behalf of the organizing committee, it is my pleasure to thank you for joining us in Philadelphia for DSN 2006.

Chandra Kintala  
General Chair, DSN 2006

## FINAL PROGRAM

Sessions A, B, C, and D are located in Rooms A, B, D, and E, respectively.

### SUNDAY, JUNE 25

0730-0800	Tutorial Registration [Conference Lobby]		
0800-1200	Tutorial 1	Tutorial 2	Tutorial 3
1200-1330	Lunch for Tutorial Registrants		
1330-1730			Tutorial 6
1800-2000	Conference Registration & Welcome Reception		

### MONDAY, JUNE 26

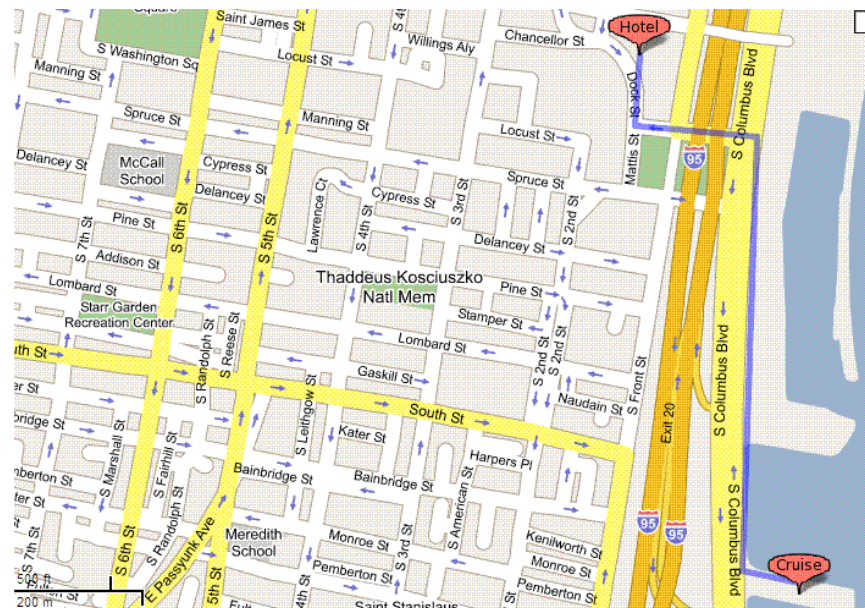
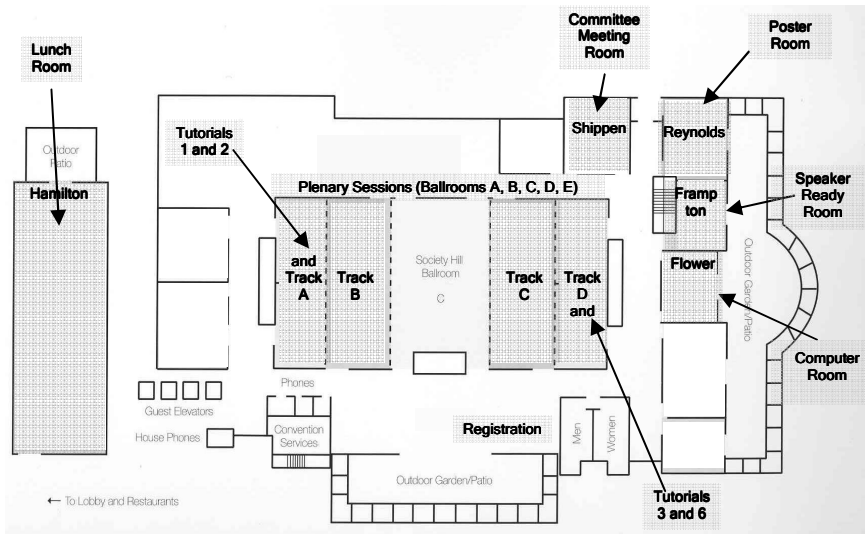
0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Opening Remarks and Keynote Address [Ballroom]			
1000-1030	Coffee Break			
1030-1200	1A: DCCS	1B: PDS	1C: Industry	1D: WASR
1200-1330	Lunch Break [Hamilton Room]			
1330-1530	2A: DCCS	2B: PDS	2C: Industry	2D: WASR
1530-1600	Coffee Break			
1600-1730	3A: DCCS	3B: PDS	3C: Industry	3D: WASR

### TUESDAY, JUNE 27

0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Plenary Panel Session [Ballroom]			
1000-1030	Coffee Break			
1030-1200	4A: DCCS	4B: PDS	4C: Fast Abs	4D: WADS
1200-1330	Lunch Break [Hamilton Room]			
1330-1530	5A: DCCS	5B: DCCS	5C: Student	5D: WADS
1530-1600	Coffee Break			
1600-1730	6A: DCCS	6B: PDS	6C: Fast Abs	6D: WADS
1800-2100	Dinner Cruise			

### WEDNESDAY, JUNE 28

0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Plenary Panel Session [Ballroom]			
1000-1030	Coffee Break			
1030-1200	7A: DCCS	7B: PDS	7D: DCCS	7E: WEEDS
1200-1330	Lunch Break [Hamilton Room]			
1330-1530	8A: DCCS	8B: PDS	8D: Fast Abs	8E: WEEDS
1530-1600	Coffee Break			
1600-1730	IEEE TC Meeting [Ballroom]			



Very special thanks to the following sponsors of DSN 2006. Without their generous support we would not be able to provide such an exciting conference:

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Thanks to the following institutions that provided generous support for publicity:	

## SUNDAY, JUNE 25

0730-0800	Tutorial Registration [Lobby]		
0800-1200	Tutorial 1 [Room A1] <b>Reliability-Aware Microprocessor Architectures</b> <i>Sarita Adve and Pradip Bose, University of Illinois and IBM TJ Watson Research</i>	Tutorial 2 [Room A2] <b>Dependable Computing over Sensor Networks</b> <i>Shivkant Mishra, University of Colorado</i>	Tutorial 3 [Room E] <b>More Reliable Software Faster and Cheaper</b> <i>John Musa, Consultant</i>
1200-1330	Lunch for Tutorial Registrants		
1330-1730			Tutorial 6 [Room E] <b>Software Dependability: What You Didn't Learn in Kindergarten</b> <i>John C. Knight and Elisabeth Strunk, University of Virginia</i>
1800-2000	Conference Registration [Conference Lobby] & Welcome Reception [Hotel Lobby]		

## MONDAY, JUNE 26

0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Opening Remarks [Ballroom] Keynote Address, " <b>Delivering Dependability: A Moving Target</b> ", <i>Dr. Ambuj Goyal, General Manger, Information Management Division, IBM</i>			
1000-1030	Coffee Break			
1030-1200	<b>DCCS Session 1A: Real-Time and Embedded Systems</b> Chair: Johan Karlsson Efficient High Hamming Distance CRCs for Embedded Networks, <i>Justin Ray and Philip Koopman</i> Memory-Conscious Reliable Execution on Embedded Chip Multiprocessors, <i>G. Chen, M. Kandemir and I. Kolcu</i> Static Analysis to Enforce Safe Value Flow in Embedded Control Systems, <i>Sumant Kowshik, Grigore Rosu and Lui Sha</i>	<b>PDS Session 1B: Dependable Storage</b> Chair: Zbigniew Kalbarczyk Dependability Analysis of Virtual Memory Systems, <i>Lakshmi N. Bairavasundaram, Andrea C. Arpaci-Dusseau and Remzi H. Arpaci-Dusseau</i> Assessment of the Effect of Memory Page Retirement on System RAS Against Hardware Faults, <i>Dong Tang, Peter Carruthers, Zuheir Totari and Michael Shapiro</i> Designing Dependable Storage Solutions for Shared Application Environments, <i>Shravan Gaonkar, Kimberly Keeton, Arif Merchant and William H. Sanders</i>	<b>Industry Session Session 1C</b> Moderator: Brendan Murphy The Other Side of Failure!, <i>Jim Johnson, Standish Group</i> Dependable Computing: A System Perspective, <i>Wendy Bartlett, Hewlett Packard</i>	<b>Workshop 1 on Applied Software Reliability (WASR)</b> <b>Session 1D:</b> Chair: Sachin Garg Reliable Multicast for Time-Critical Systems, <i>M. Balakrishnan and K. Birman</i> Reliability Requirements of Wireless Sensor Networks for Dynamic Structural Monitoring, <i>M. Cinque, D. Cotroneo, G. De Caro and M. Pelella</i> Reliability Requirements for Infrastructure System Sensor Networks, <i>M. Bigrigg</i> Panel: "Reliability Requirements for Emerging Applications"
1200-1330	Lunch Break [Hamilton Room]			

**MONDAY, JUNE 26**

1330-1530	<p><b>DCCS</b>  <b>Session 2A: Safety-Critical Systems</b>                  Chair: Neeraj Suri</p> <p>The Startup Problem in Fault-Tolerant Time-Triggered Communication, <i>Wilfried Steiner and Hermann Kopetz</i></p> <p>A Reconfigurable Generic Dual-Core Architecture, <i>Thomas Kottke and Andreas Steininger</i></p> <p>A Dependable System Architecture for Safety-Critical Respiratory-Gated Radiation Therapy, <i>Gregory Sharp and Nagarajan Kandasamy</i></p> <p>User Interface Defect Detection by Hesitation Analysis, <i>Robert W. Reeder and Roy A. Maxion</i></p>	<p><b>PDS</b>  <b>Session 2B: Attack Prevention and Mitigation</b>                  Chair: Paulo Verissimo</p> <p>A Statistical Analysis of Attack Data to Separate Attacks, <i>Michel Cukier, Robin Berthier, Susmit Panjwani and Stephanie Tan</i></p> <p>VoIP Intrusion Detection Through Protocol State Machines, <i>Hemant Sengar, Duminda Wijesekera, Haining Wang and Sushil Jajodia</i></p> <p>Mitigating Active Attacks Towards Client Networks Using the Bitmap Filter, <i>Chun-Ying Huang, Kuan-Ta Chen and Chin-Laung Lei</i></p> <p>Accurate and Automated System Call Policy-Based Intrusion Prevention, <i>Lap Chung Lam, Wei Li and Tzi-cker Chiueh</i></p>	<p><b>Industry Session</b>  <b>Session 2C</b>                  Moderator: Ram Chillarege</p> <p>Panel: "Minimizing the Effects of Murphy's Law", <i>Siddhartha Alladi, Alladi Computing; Jeremy Winter, MSN; Fabrizio Petrini, Pacific National Lab; Robert Cline, Sun Gard; Ron Lesan, AOL; Wesley Story, Sprint</i></p>	<p><b>Workshop 1 on Applied Software Reliability (WASR)</b>  <b>Session 2D</b>                  Chair: Yennun Huang</p> <p>Predicting Field Defects Based on Software Test Results, <i>V.B. Mendiratta and J.M. Souza</i></p> <p>Providing Automated Detection of Problems in Virtualized Servers using Monitor framework, <i>G. Khanna, S. Bagchi, K. Beaty, A. Kochut and G. Ken</i></p> <p>How the Hidden Hand Shapes the Market for Software Reliability, <i>K. Birman, C. Chandrasekaran, D. Dolev and R. van Renesse</i></p> <p>Model-Centric Development of Highly Available Software Systems, <i>R.W. Buskens and O.J. Gonzalez</i></p> <p>Panel: "The Quest for Reliable Software: Paradigms and Factors Driving Industry"</p>
1530-1600 Coffee Break				
1600-1730	<p><b>DCCS</b>  <b>Session 3A: Architecture and Operating Systems</b>                  Chair: A. J. Kleinosowski</p> <p>Dynamic Verification of Memory Consistency in Cache-Coherent Multithreaded Computer Architectures, <i>Albert Meixner and Daniel J. Sorin</i></p> <p>Automatic Instruction-Level Software-Only Recovery Methods, <i>Jonathan Chang, George A. Reis and David I. August</i></p> <p>Exploring Fault-Tolerant Network-on-Chip Architectures, <i>Dongkook Park, Chrysostomos Nicopoulos, Jongman Kim, N. Vijaykrishnan and Chita R. Das</i></p>	<p><b>PDS</b>  <b>Session 3B: Dependability Models</b>                  Chair: Boudewijn Haverkort</p> <p>BlueGene/L Failure Analysis and Prediction Models, <i>Yinglung Liang, Yanyong Zhang, Morris Jette, Anand Sivasubramaniam and Ramendra Sahoo</i></p> <p>Performance Assurance via Software Rejuvenation: Monitoring, Statistics and Algorithms, <i>Alberto Avritzer, Andre Bondi, Michael Grottke, Kishor Trivedi and Elaine J. Weyuker</i></p> <p>Automatic Recovery Using Bounded Partially Observable Markov Decision Processes, <i>Kaustubh R. Joshi, Matti A. Hiltunen, William H. Sanders and Richard D. Schlichting</i></p>	<p><b>Industry Session</b>  <b>Session 3C</b>                  Moderator: Lisa Spainhower</p> <p>Panel: "Tomorrow's Technology: Friend or Foe?"; <i>Brendan Murphy, Microsoft; Greg Astfalk, Hewlett Packard; Brent Miller, IBM; Erik Grimmelmann, SWN Communications</i></p>	<p><b>Workshop 1 on Applied Software Reliability (WASR)</b>  <b>Session 3D</b>                  Chair: Ken Birman</p> <p>Big Gap from Academic Response to Industry's Demand for Optimized Engineering Efficacy, <i>C. H. Pham, F. Lin, N. Gupta and K. Ma</i></p> <p>Be Good (Reliable) or Be Careful (Fault Tolerant), <i>H. Hecht</i></p> <p>Integrating Software Reliability and Software Engineering in Education (or Software Reliability Begins in the Classroom), <i>L. Bernstein and C. Kintala</i></p> <p>Closing the Gap in Failure Analysis, <i>B. Murphy, M. Garzia and N. Suri</i></p> <p>Panel: "Closing the Gap between Academic Research and Industry Needs"</p>
1730-1830	<p><b>Large Scale Experiments in Dependability: Defining a Research Agenda</b>                  Includes discussion of GENI                  (All participants are invited.)</p>			

**TUESDAY, JUNE 27**

0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Plenary Panel Session [Ballroom] <b>"Coordinated, Malicious Cyber and Physical Attacks on National Infrastructures"</b> , <i>Moderator: Dr. Jeffrey Voas, SAIC Corporation</i>			
1000-1030	Coffee Break			

**TUESDAY, JUNE 27**

<p>1030-1200</p>	<p><b>DCCS</b>  <b>Session 4A: Byzantine Faults</b>  Chair: Marcos K. Aguilera</p> <p>Scaling Byzantine Fault-Tolerant Replication to Wide Area Networks, <i>Yair Amir, Claudiu Danilov, Danny Dolev, Jonathan Kirsch, John Lane, Cristina Nita-Rotaru, Josh Olsen and David Zage</i></p> <p>Optimal Resilience for Erasure-Coded Byzantine Distributed Storage, <i>Christian Cachin and Stefano Tessaro</i></p> <p>Lucky Read/Write Access to Robust Atomic Storage, <i>Rachid Guerraoui, Ron R. Levy and Marko Vukolic</i></p>	<p><b>PDS</b>  <b>Session 4B: Attack Analysis</b>  Chair: Bill Sanders</p> <p>Using Attack Injection to Discover New Vulnerabilities, <i>Nuno Neves, João Antunes, Miguel Correia, Paulo Verissimo and Rui Neves</i></p> <p>Assessing the Attack Threat due to IRC Channels, <i>Robert Meyer and Michel Cukier</i></p> <p>An Approach for Detecting and Distinguishing Errors versus Attacks in Sensor Networks, <i>Claudio Basile, Meeta Gupta, Zbigniew Kalbarczyk and Ravi K. Iyer</i></p>	<p><b>Fast Abstracts</b>  <b>Session 4C</b>  Chair: Bojan Cukic</p> <p>OCDFI: On-Chip Debug and Fault Injection, <i>André Fidalgo, Gustavo Alves and José Ferreira</i></p> <p>Defining Steady-State Service Level Agreeability using Semi-Markov Process, <i>Ranjith Vasireddy and Kishor S Trivedi</i></p> <p>Architectural Reconfiguration of Software Systems using Atomic Actions, <i>Rogério de Lemos</i></p> <p>Optimistic Open Groups for Fault-Tolerant Replication, <i>Wenbo Zhu and Murray Woodside</i></p>	<p><b>Workshop 2 on Architecting Dependable Systems (WADS)</b>  <b>Session 4D: Software Architectures and Dependability</b>  Chair: Cristina Gacek</p> <p>Keynote Talk, <i>Professor Mary Shaw (Carnegie Mellon University)</i></p> <p>Discussion</p>
<p align="center">Lunch Break [Hamilton Room]</p>				
<p>1200-1330</p>	<p><b>DCCS</b>  <b>Session 5A: Consensus and Leader Election</b>  Chair: Tohru Kikuno</p> <p>One-Step Consensus with Zero-Degradation, <i>Dan Dobre and Neeraj Suri</i></p> <p>Consensus with Byzantine Failures and Little System Synchrony, <i>Marcos K. Aguilera, Carole Delporte-Gallet, Hugues Fauconnier and Sam Toueg</i></p> <p>Solving Atomic Broadcast with Indirect Consensus, <i>Richard Ekwall and André Schiper</i></p> <p>Eventual Leader Election with Weak Assumptions on Initial Knowledge, Communication Reliability, and Synchrony, <i>Antonio Fernández, Ernesto Jiménez and Michel Raynal</i></p>	<p><b>DCCS</b>  <b>Session 5B: Intrusion Detection and Tolerance</b>  Chair: Mohamed Kaaniche</p> <p>Hotspots: The Root Causes of Non-Uniformity in Self-Propagating Malware, <i>Evan Cooke, Z. Morley Mao and Farnam Jahanian</i></p> <p>A Multi-Resolution Approach for Worm Detection and Containment, <i>Vyas Sekar, Yinglian Xie, Michael K. Reiter and Hui Zhang</i></p> <p>Honeypot-Aware Advanced Botnet Construction and Maintenance, <i>Cliff C. Zou and Ryan Cunningham</i></p> <p>Barbarians in the Gate: An Experimental Validation of NIC-Based Distributed Firewall Performance and Flood Tolerance, <i>Michael Ihde and William H. Sanders</i></p>	<p><b>Student Forum</b>  <b>Session 5C</b>  Chair: Christof Fetzer</p> <p>Power-aware Fault Tolerance Compilation: Using Less Branches to Reduce Power Dissipation and Improve Performance, <i>Gao Long</i></p> <p>Increasing Data Resilience of Mobile Devices with a Collaborative Backup Service, <i>Damien Martin-Guilerez</i></p> <p>Behavior-Driven Testing of Windows Device Drivers, <i>Constantin Sarbu</i></p> <p>Revisiting Fletcher and Adler Checksums, <i>Theresa Maxino</i></p> <p>Intrusion Detection in Databases, <i>José Fonseca</i></p> <p>Towards Dynamically Reconfigurable Hard-Real-Time Communication for Embedded Mechatronic Systems, <i>André Luiz de F. Francisco</i></p>	<p><b>Workshop 2 on Architecting Dependable Systems (WADS)</b>  <b>Session 5D: Fault Tolerance</b>  Chair: Alexander Romanovsky</p> <p>Invited Talk: The SAE Architecture Analysis and Description Language (AADL) Standard: A Basis for Architecture-Driven Embedded Systems Engineering, <i>Joyce L Tokar (Pyrrhus Software)</i></p> <p>An Evaluation of Fault Tolerant TCP-Splice Based Web Server Architectures, <i>Manish Marwah, Jacob Delgado, Shivakant Mishra and Christof Fetzer</i></p> <p>Idealised Fault Tolerant Architectural Element, <i>Rogério de Lemos</i></p> <p>Fault-tolerant Smart Sensor Architecture for Integrated Modular Avionics, <i>Stefan Schneelee, Klaus Echtle, Josef Schalk</i></p>
<p>Multidisciplinary Reliability Modeling for Sensor Network Requirements, <i>Michael W. Bigrigg</i></p> <p>A New Approach for Fault Tolerant and Secure Distributed Storage, <i>Arun Subbiah</i></p> <p>Dependable and Schedulable Online-Testing Framework for Real-Time Embedded Applications in CLI, <i>Okehee Goh</i></p> <p>Application-Transparent Distributed Checkpoint-Recovery for OpenSSI, Kernel-Level Single-System-Image Linux Clustering Platform, <i>Aleksander Korzynski</i></p> <p>Automatic Generation of Robustness and Security Properties from Program Source Code, <i>Mithun Acharya</i></p>				

## TUESDAY, JUNE 27

1530-1600	Coffee Break			
1600-1730	<p><b>DCCS</b>  <b>Session 6A: Storage Systems</b>                      Chair: James Plank</p> <p>HoVer Erasure Codes for Disk Arrays, <i>James Lee Hafner</i></p> <p>Storage Allocation in Unreliable Peer-to-Peer Systems, <i>John A. Chandy</i></p> <p>Reliability for Networked Storage Nodes, <i>KK Rao, James Lee Hafner and Richard A. Golding</i></p>	<p><b>PDS</b>  <b>Session 6B: Measuring and Modeling</b>                      Chair: Michel Cukier</p> <p>A Component-Level Path Composition Approach for Efficient Transient Analysis of Large CTMCs, <i>Vinh V. Lam, Peter Buchholz and William H. Sanders</i></p> <p>Evaluating the Performability of Systems with Background Jobs, <i>Qi Zhang, Alma Riska, Erik Riedel, Ningfang Mi and Evgenia Smirni</i></p> <p>A Contribution Towards Solving the Web Workload Puzzle, <i>Katerina Goseva-Popstojanova, Fengbin Li, Xuan Wang and Amit Sangle</i></p>	<p><b>Fast Abstracts</b>  <b>Session 6C</b>                      Chair: Cristina Nita-Rotaru</p> <p>Fault-Tolerant Algorithms on SoCs - A Case Study, <i>Andreas Steininger, Matthias Függer, Ulrich Schmid and Gottfried Fuchs</i></p> <p>When is the Right Time to Inject an Error?, <i>Andreas Johansson, Constantin Sărbu and Neeraj Suri</i></p> <p>Address Space Layout Permutation, <i>Chongkyung Kil, Jinsuk Jun, Christopher Bookholt and Jun Xu</i></p> <p>A Byzantine Resilient Distributed Position Service, <i>Cristina Nita-Rotaru, Josh Olsen and David Zage</i></p>	<p><b>Workshop 2 on Architecting Dependable Systems (WADS)</b>  <b>Session 6D: Infrastructure for Dynamic Change</b>                      Chair: Rogerio de Lemos</p> <p>Invited Talk: Dependability Services in the EASIS Software Platform, <i>Martin Hiller (Volvo Technology Corporation)</i></p> <p>Impact-Sensitive Framework for Dynamic Change-Management, <i>Tudor Dumitras, Daniela Rosu, Asit Dan and Priya Narasimhan</i></p> <p>Discussion                      Wrap-up / Future Directions</p>
	<p>Reliability Analysis of Digital Control Equipment for Nuclear Power Plant, <i>Ji-Young Kim, Dong-Young Lee and Joon Lyou</i></p> <p>Simplified Automated Fault Injection (SAFI) - A Solution to Facilitate Unit Testing in Large Scaled Test Environment, <i>Nilay Gupta and Christopher Hoang Pham</i></p> <p>Error Detection in Service-Oriented Distributed Systems, <i>Andrei Korostelev, Johan Lukkien and Jan Nesvadba</i></p> <p>Challenges Related to the Development and Approval of Essential Systems Onboard Ships and Offshore Vessels, <i>Torbjorn Skramstad and Lars Bratthall</i></p> <p>On Exploiting Symmetry To Verify Distributed Protocols, <i>Marco Serafini, Neeraj Suri and Péter Bokor</i></p> <p>Locating File Processing Vulnerabilities, <i>Nuno Ferreira Neves</i></p>			
1800-2100	Dinner Cruise			

## WEDNESDAY, JUNE 28

0800-0830	Conference Registration and Continental Breakfast [Lobby]			
0830-1000	Plenary Panel Session [Ballroom] <b>"Global Dependability Collaborations - Challenges and Successes"</b> , <i>Moderator: Jaynarayan Lala, Raytheon</i>			
1000-1030	Coffee Break			

**WEDNESDAY, JUNE 28**

<p>1030-1200</p>	<p><b>DCCS</b>  <b>Session 7A: Complex and Large Scale Systems</b>          Chair: Elmootazbellah Elnozahy          A Large-Scale Study of Failures in High-Performance-Computing Systems, <i>Bianca Schroeder and Garth A. Gibson</i>          Tracking Probabilistic Correlation of Monitoring Data for Fault Detection and Isolation in Complex Systems, <i>Zhen Guo, Guofei Jiang, Haifeng Chen and Kenji Yoshihira</i>          Efficiently Detecting All Dangling Pointer Uses in Production Servers, <i>Dinakar Dhurjati and Vikram Adve</i></p>	<p><b>PDS</b>  <b>Session 7B: Multiple-Server Systems</b>          Chair: Paul Ezhilchelvan          Empirical and Analytical Evaluation of Systems with Multiple Unreliable Servers, <i>J. Palmer and I. Mitrani</i>          R-Opus: A Composite Framework for Application Performability and QoS in Shared Resource Pools, <i>Ludmila Cherkasova and Jerry Rolia</i>          Cost-Effective Configuration of Content Resiliency Services Under Correlated Failures, <i>Jinliang Fan, Dimitrios Pendarakis, Zhen Liu and Tianying Chang</i></p>	<p><b>DCCS</b>  <b>Session 7D: VLSI</b>          Chair: Cristian Constantinescu          In-Register Duplication: Exploiting Narrow-Width Value for Improving Register File Reliability, <i>Jie Hu, Shuai Wang and Sotirios Ziavras</i>          Run-Time Reconfiguration for Emulating Transient Faults in VLSI Systems, <i>David de Andrés, Juan Carlos Ruiz, Daniel Gil and Pedro Gil</i>          CADRE: Cycle-Accurate Deterministic Replay for Processor Debugging, <i>Smruti Sarangi, Brian Greskamp and Josep Torrellas</i></p>	<p><b>Workshop 3 on Empirical Evaluation of Dependability and Security (WEEDS)</b>  <b>Session 7D: Empirical Evaluation of DEPENDABILITY</b>          Chair: Ioana Rus  <b>Safety</b>          Experience and Lessons Learned With Quantitative Safety and Dependability Assessment of Industrial Safety Critical Systems, <i>Carl R. Elks and Barry W. Johnson</i>  <b>Fault Injection</b>          A Field Data Study on the Use of Software Metrics to Define Representative Fault Distribution, <i>Regina Moraes, João Durães, Eliane Martins and Henrique Madeira</i>  <b>Reliability</b>          Empirical Testing of the Handling of a Reliability-Aware Storage Device, <i>Michael W. Bigrigg</i>          Discussion</p>
<p>1200-1330</p>	<p align="center">Lunch Break [Hamilton Room]</p>			
<p>1330-1530</p>	<p><b>DCCS</b>  <b>Session 8A: Networking</b>          Chair: Farnam Jahanian          Collecting and Analyzing Failure Data of Bluetooth Personal Area Networks, <i>Marcello Cinque, Domenico Cotroneo and Stefano Russo</i>          Improving BGP Convergence Delay for Large-Scale Failures, <i>Amit Sahoo, Krishna Kant and Prasant Mohapatra</i>          Secure Split Assignment Trajectory Sampling: A Malicious Router Detection System, <i>Sihyung Lee, Tina Wong and Hyong S. Kim</i>          A General Framework for Scalability and Performance Analysis of DHT Routing Systems, <i>Joseph S. Kong, Jesse S. A. Bridgewater and Vwani P. Roychowdhury</i></p>	<p><b>PDS</b>  <b>Session 8B: Distributed Algorithms</b>          Chair: Michael Reiter          High Throughput Uniform Total Order Broadcast for Cluster Environments, <i>Rachid Guerraoui, Ron R. Levy, Bastian Pochon and Vivien Quema</i>          Improving Fault Resilience of Overlay Multicast for Media Streaming, <i>Guang Tan, Stephen A. Jarvis and Daniel P. Spooner</i>          Randomized Intrusion-Tolerant Asynchronous Services, <i>Henrique Moniz, Nuno Ferreira Neves, Miguel Correia and Paulo Veríssimo</i>          A Performance Study on the Signal-on-Fail Approach to Imposing a Total Order in the Streets of Byzantium, <i>Qurat-ul-Ain Inayat and Paul Devadoss Ezhilchelvan</i></p> <p><i>Ramasamy, Christian Cachin, Adnan Agbaria and William H. Sanders</i>          FPGA Implementation of the Illinois Reliability and Security Engine, <i>Peter F. Klemperer, Reza Farivar, Giacinto Paolo Saggese, Nithin Nakka, Zbigniew Kalbarczyk and Ravishankar K. Iyer</i>          SPACEDIVE: A Distributed Intrusion Detection System for Voice-over-IP Environments, <i>Vinita Apte, Yu-Sung Wu, Saurabh Bagchi, Sachin Garg and Navjot Singh</i>          Modeling Probabilistic Diagnosis Parameters, <i>Gunjan Khanna, Yu Cheng and Saurabh Bagchi</i>          Modeling Cascading and Escalating Outages in Interdependent Critical Infrastructures, <i>Jean-Claude Laprie, Karama Kanoun and Mohamed Kaaniche</i>          A Self-checking and Reconfigurable Framework for Application Reliability Exploiting Execution Characteristics, <i>Long Wang, Zbigniew Kalbarczyk and Ravi. Iyer</i>          A Discussion of Performance Optimizations for Compiler-Based Buffer Overflow Instrumentation, <i>Timothy Tsai</i>          A Mediator System for Improving Dependability of Web Services, <i>Yuhui Chen and Alexander Romanovsky</i></p>	<p><b>Fast Abstracts</b>  <b>Session 8C:</b>          Chair: Saurabh Bagchi          An Evolutionary Operational Profiles Approach for Integration Tests, <i>Maria de Fátima Mattiello-Francisco</i>          Comparing Process and Thread Redundancy in CMP on Energy and Reliability, <i>Dakai Zhu and Hakan Aydin</i>          Aspects Made Explicit for Safe Transactional Semantics, <i>Kevin Hoffman and Patrick Eugster</i>          Automated Derivation of Application Aware Error Detectors using Static Analysis, <i>Karthik Pattabiraman, Zbigniew Kalbarczyk and Ravi K. Iyer</i>          The Parsimonious Approach to Constructing Fault-Tolerant Protocols, <i>HariGovind V.</i></p>	<p><b>Workshop 3 on Empirical Evaluation of Dependability and Security (WEEDS)</b>  <b>Session 8D: Empirical Evaluation of SECURITY</b>          Chair: Michel Cukier          Towards Security Evaluation based on Evidence Information Collection and Impact Analysis, <i>Reijo Savola and Juha Rönning</i>          Empirical Analysis and Statistical Modeling of Attack Processes based on Honeypots, <i>Mohamed Kaâniche, Eric Alata, Vincent Nicomette, Yves Deswarte and Marc Dacier</i>          Can We Quantitatively Assess Security?, <i>Boudewijn R. Haverkort</i>          Compromise Modes and Effects Analysis, <i>Bradley J. Wood, Rico R. Valdez and Justin, M. Parsley</i></p>

## WEDNESDAY, JUNE 28

1530-1600	Coffee Break
1600-1730	<b>IEEE TC-FTC Business Meeting [Ballroom]</b> (All participants are invited.)

### Organizing Committee

General Chair:	Chandra Kintala (Stevens Inst. of Tech, USA)
Conference Coordinator:	David Taylor (U. of Waterloo, Canada)
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### Dependable Computing and Communication Symposium (DCCS)

Program Chair:	Lorenzo Alvisi (U. of Texas, USA)																																											
Program Committee:	<table style="width: 100%; border: none;"> <tr> <td style="width: 33%;">M. Aguilera (H.P. Labs, USA)</td> <td style="width: 33%;">F. Jahanian (U. of Michigan, USA)</td> <td style="width: 33%;">A. Pataricza (Budapest U. of Techn. and Econ., Hungary)</td> </tr> <tr> <td>O. Babaoglu (U. of Bologna, Italy)</td> <td>R. Jimenez (TU Madrid, Spain)</td> <td>W. Sanders (UIUC, USA)</td> </tr> <tr> <td>R. Baldoni (U. of Rome, Italy)</td> <td>M. Kaaniche (LAAS-CNRS, France)</td> <td>J. Plank (U. of Tennessee, Knoxville, USA)</td> </tr> <tr> <td>D. Blough (Georgia Tech, USA)</td> <td>S. Keckler (UT Austin, USA)</td> <td>D. Powell (LAAS-CNRS, France)</td> </tr> <tr> <td>A. Bondavalli (U. of Firenze, Italy)</td> <td>I. Keidar (Technion, Israel)</td> <td>M. Reiter (CMU, USA)</td> </tr> <tr> <td>C. Cachin (IBM Research, Switzerland)</td> <td>A. M. Kermerrec (INRIA-IRISA, France)</td> <td>A. Rowstron (Microsoft Research, UK)</td> </tr> <tr> <td>M. Castro (Microsoft Research, UK)</td> <td>AJ KleinOowski (IBM Research, USA)</td> <td>R. Sanders (UIUC, USA)</td> </tr> <tr> <td>W. Chen (Microsoft Research, PRC)</td> <td>P. Koopman (CMU, USA)</td> <td>R. Schlichting (AT&amp;T Research, USA)</td> </tr> <tr> <td>E. Duarte (Federal U. of Paraná, Brazil)</td> <td>M. Lyu (Chinese U. of Hong Kong, HK)</td> <td>V. Schmatikov (UT Austin, USA)</td> </tr> <tr> <td>N. Ferreira Neves (U. of Lisboa, Portugal)</td> <td>H. Madeira (U. of Coimbra, Portugal)</td> <td>L. Strigini (City U. of London, UK)</td> </tr> <tr> <td>C. Fetzer (TU Dresden, Germany)</td> <td>D. Malkhi (Hebrew U., Israel)</td> <td>N. Suri (TU Darmstadt, Germany)</td> </tr> <tr> <td>S. Gribble (UW Seattle, USA)</td> <td>K. Marzullo (UC San Diego, USA)</td> <td>R. van Renesse (Cornell, USA)</td> </tr> <tr> <td></td> <td>R. Moxion (CMU, USA)</td> <td>H. Vin (TRDDC, India)</td> </tr> <tr> <td></td> <td>G. Muller (École des Mines de Nantes, France)</td> <td>Y.-M. Wang (Microsoft Research, USA)</td> </tr> </table>		M. Aguilera (H.P. Labs, USA)	F. Jahanian (U. of Michigan, USA)	A. Pataricza (Budapest U. of Techn. and Econ., Hungary)	O. Babaoglu (U. of Bologna, Italy)	R. Jimenez (TU Madrid, Spain)	W. Sanders (UIUC, USA)	R. Baldoni (U. of Rome, Italy)	M. Kaaniche (LAAS-CNRS, France)	J. Plank (U. of Tennessee, Knoxville, USA)	D. Blough (Georgia Tech, USA)	S. Keckler (UT Austin, USA)	D. Powell (LAAS-CNRS, France)	A. Bondavalli (U. of Firenze, Italy)	I. Keidar (Technion, Israel)	M. Reiter (CMU, USA)	C. Cachin (IBM Research, Switzerland)	A. M. Kermerrec (INRIA-IRISA, France)	A. Rowstron (Microsoft Research, UK)	M. Castro (Microsoft Research, UK)	AJ KleinOowski (IBM Research, USA)	R. Sanders (UIUC, USA)	W. Chen (Microsoft Research, PRC)	P. Koopman (CMU, USA)	R. Schlichting (AT&T Research, USA)	E. Duarte (Federal U. of Paraná, Brazil)	M. Lyu (Chinese U. of Hong Kong, HK)	V. Schmatikov (UT Austin, USA)	N. Ferreira Neves (U. of Lisboa, Portugal)	H. Madeira (U. of Coimbra, Portugal)	L. Strigini (City U. of London, UK)	C. Fetzer (TU Dresden, Germany)	D. Malkhi (Hebrew U., Israel)	N. Suri (TU Darmstadt, Germany)	S. Gribble (UW Seattle, USA)	K. Marzullo (UC San Diego, USA)	R. van Renesse (Cornell, USA)		R. Moxion (CMU, USA)	H. Vin (TRDDC, India)		G. Muller (École des Mines de Nantes, France)	Y.-M. Wang (Microsoft Research, USA)
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R. Guerraoui (EPFL/MIT, Switzerland)	France	USA
D. Johnson (Rice U., USA)	T. Nanya (U. of Tokyo, Japan)	H. Yu (Intel Research, USA)
M. Hiller (Volvo, Sweden)		L. Zhou (Microsoft Research, USA)
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### Performance and Dependability Symposium (PDS)

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### Student Forum

Chair:	Christof Fetzer (TU Dresden, Germany)
Members:	Tadashi Dohi (Hiroshima University, Japan) Shivakant Mishra (University of Colorado, Boulder, USA)

### Fast Abstracts

Chair:	Saurabh Bagchi (Purdue University, IN, USA)
Members:	Cristina Nita-Rotaru (Purdue University, IN, USA) Bojan Cukic (West Virginia University, USA) Nuno Ferreira Neves (University of Lisboa, Portugal)

### Workshops

Chair:	Neeraj Suri (TU Darmstadt, Germany)
Members:	Lisa Spainhower (IBM, USA) Phil Koopman (CMU, USA) Christof Fetzer (TU Dresden, Germany)

### Industry Session

Chair:	Lisa Spainhower (IBM, USA)
Members:	Wendy Bartlett (HP, USA) Ram Chillarege (Chillarege, Inc., USA) Brendan Murphy (Microsoft, UK)